

Title: WAFER BONDED EPITAXIAL TEMPLATES FOR SILICON HETEROSTRUCTURES Inventor(s): Henry A. ATWATER et al. Docket No.: 047071-0109

OPTO-ELECTRONIC, HIGH-GAIN WAFER
BONDED VIRTUAL SUBSTRATES

SUBSTRATE FOR THIN FILM
DEVICE FABRICATION
WITH HANDLE-SUBSTRATE
DEVICE COMPONENTS

100

FIG. 1

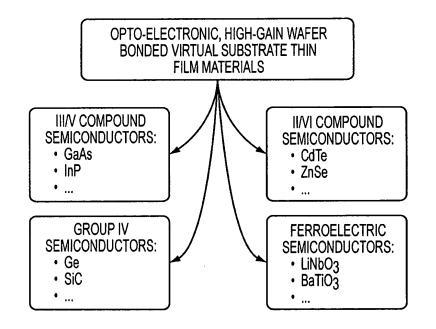


FIG. 2

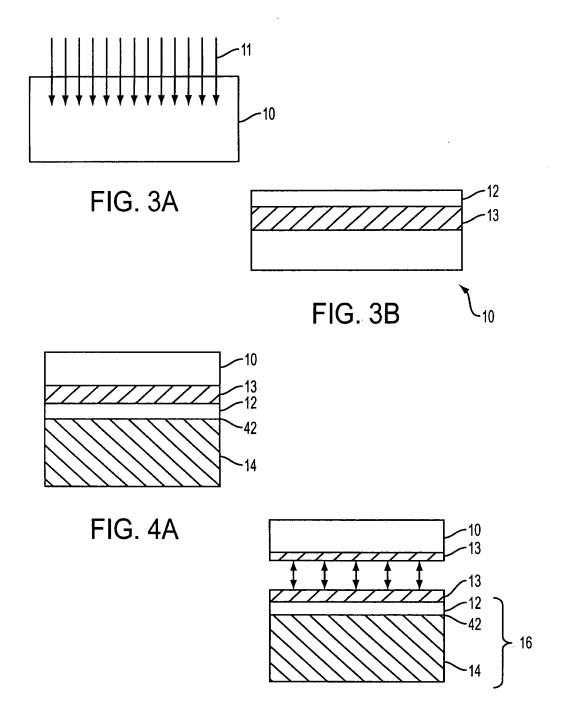


FIG. 4B

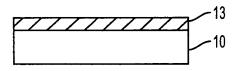


FIG. 5A

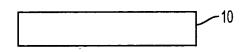


FIG. 5B

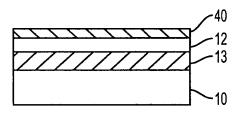


FIG. 6A

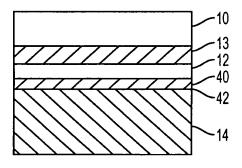


FIG. 6B

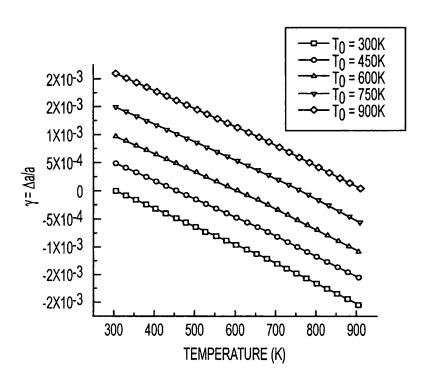


FIG. 7

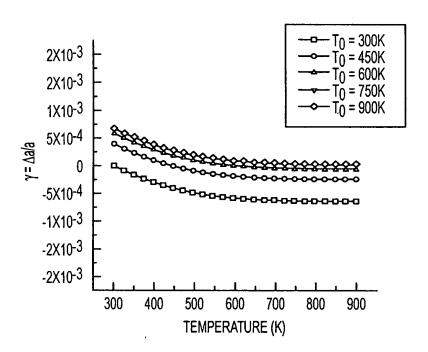
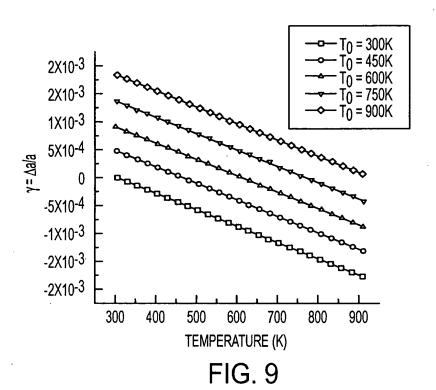
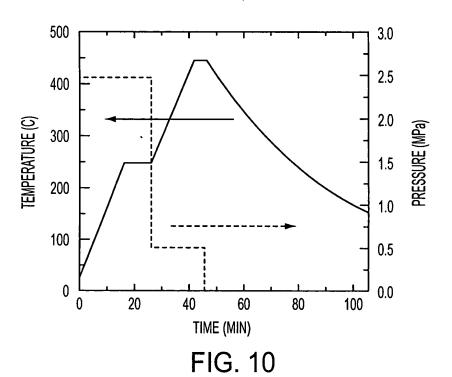


FIG. 8





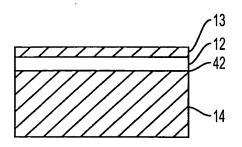


FIG. 11A

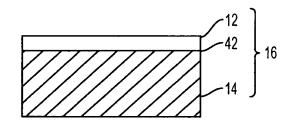


FIG. 11B

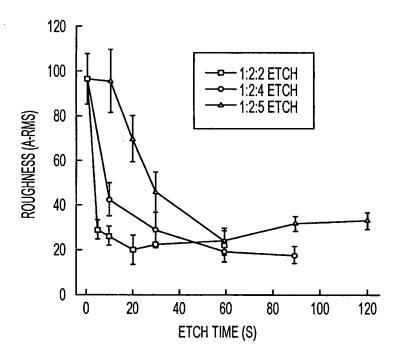
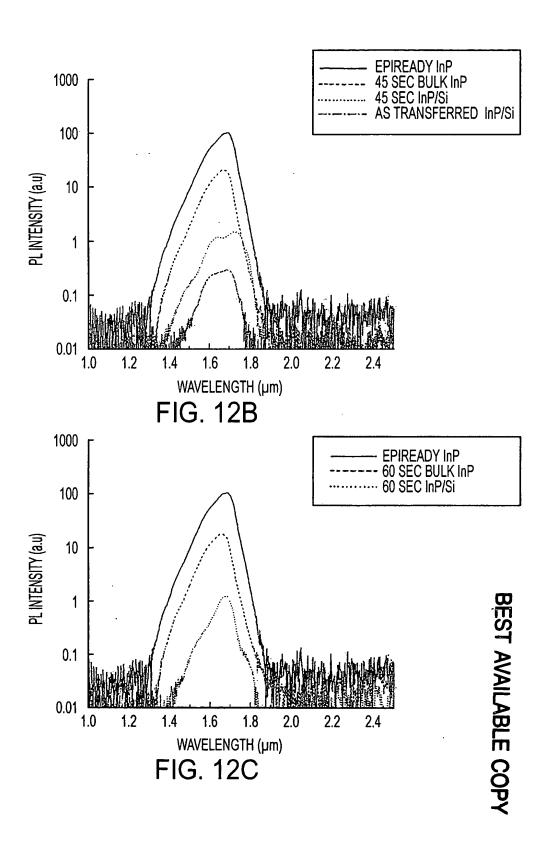
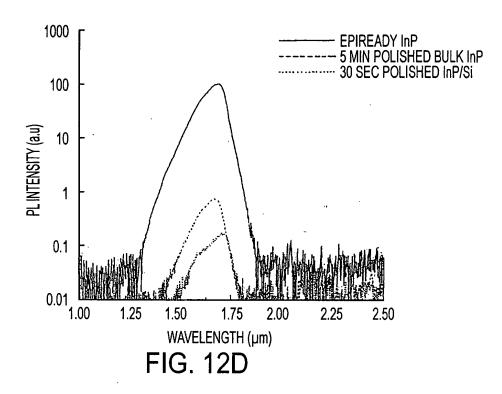


FIG. 12A



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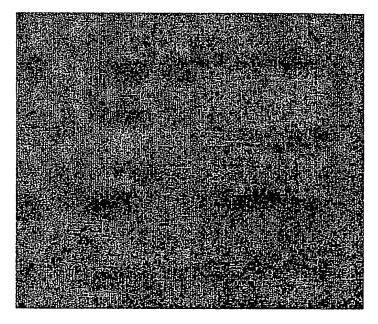
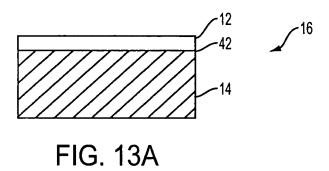


FIG. 12E

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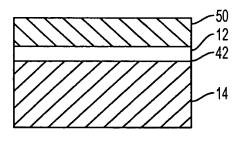


FIG. 13B

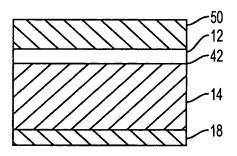


FIG. 14